Remarks

In response to the Office Action dated September, 22, 2004, the examiner rejected claim 13 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. This rejection is respectfully traversed. Cernea (6560146) discloses an array of memory cells used to hold read data or write data. Claim 13 of this invention together with the specification describe a memory system that saves power by storing several read cycles and then writing the acquired data to MRAM. Cernea (6560146) does not anticipate saving power by storing several read cycles and then writing the acquired data to MRAM.

The examiner rejected claims 14-16 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. These rejections are respectfully traversed. The rejection of claims 14-16 are no longer applicable because Claim 13 is in condition for allowance and claims 14-16 are dependent on Claim 13.

The examiner rejected claim 17 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. This rejection is respectfully traversed. Cernea (6560146) discloses an array of memory cells used to hold read data or write data. Claim 17 of this invention together with the specification describe a memory system that saves power by storing several read cycles and then writing the acquired data to MRAM. Cernea (6560146) does not anticipate saving power by storing several read cycles and then writing the acquired data to MRAM.

The examiner rejected claims 18-20 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. These rejections are respectfully traversed. The rejection of claims 18-20 are no longer applicable because Claim 17 is in condition for allowance and claims 18-20 are dependent on Claim 17.

The examiner rejected claim 21 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. This rejection is respectfully traversed. Cernea (6560146) discloses an array of memory cells used to hold read data or write data. Claim 21 of this invention together with the specification describe a memory system that saves

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power by storing several read cycles and then writing the acquired data to MRAM. Cernea (6560146) does not anticipate saving power by storing several read cycles and then writing the acquired data to MRAM.

The examiner rejected claims 22-24 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. These rejections are respectfully traversed. The rejection of claims 22-24 are no longer applicable because Claim 21 is in condition for allowance and claims 22-24 are dependent on Claim 21.

The examiner rejected Claim 25 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. This rejection is respectfully traversed. Cernea (6560146) discloses an array of memory cells used to hold read data or write data. Claim 25 of this invention together with the specification describe a memory system that saves power by storing several read cycles and then writing the acquired data to MRAM. Cernea (6560146) does not anticipate saving power by storing several read cycles and then writing the acquired data to MRAM.

The examiner rejected claims 1-12 under 35 U.S.C. 102(a) as being anticipated by Cernea, U.S. Patent No. 6,560,146. This rejection is respectfully traversed. Cernea (6560146) discloses an array of memory cells used to hold read data or write data. Claims 1-12 of this invention together with the specification describe a memory system that saves power by storing several read cycles and then writing the acquired data to MRAM. Cernea (6560146) does not anticipate saving power by storing several read cycles and then writing the acquired data to MRAM.

The examiner did not rely on prior art, Nakayama et. al. (6714477), Perner et al. (6717874), Kunikiyo (6754098), and Riedel (6732241) but considered them pertinent to the application. None of them refer to saving power then writing data to an MRAM.

Reconsideration of the referenced application is requested.

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Respectfully submitted,

Reg. Number 48,369

December 13, 2004 Fort Collins, CO 80525 (970) 898-6530